

SDIO GPS Receiver SD-502



Great SD-502 is a GPS Receiver with SDIO and an integrated built-in memory, which provides an impressive data capacity that allows to organize, store, and share files or documents. Its 512MB of built-in memory also allows for tracking signals and large map data according to the mapping software usage. With our manufacturing technology along with the cutting edge, SIRF Star III chipset, this receiver has provided higher sensitivity, lower power usage and more rapid time-to-fix!

Features :

1. SIRF Star III high performance and low power consumption chipset
2. Built-in high sensitivity active GPS antenna
3. LED to show GPS fix or not fix
4. Built-in super capacitor to reserve system data for rapid satellite acquisition
5. SDIO interface
6. Operating platform: Pocket PC 2002/2003, Win CE, Win CE.Net, WM2003
7. Antenna can be folded from 0° ~ 180°
8. Base on Bsquare SDIO Now!
9. Built-in memory size: 512MB.



SDIO GPS Receiver

SD-502

SD-502 Specification

Electrical Characteristics

Frequency	L1, 1575.42 MHz
C/A Code	1.023 MHz chip rate
Channels	20 channel all-in-view tracking

Accuracy

Position Horizontal	10 meters, 2D RMS 1-5 meters 2D RMS, WAAS corrected
Velocity	0.1 m/sec
Time	1 micro-second synchronized to GPS time

Datum

Datum	Default :WGS-84
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Acquisition Time

Reacquisition	0.1 sec., Average
Hot start	8 sec., Average
Warm start	38 sec., Average
Cold start	42 sec., Average

Dynamic Conditions

Altitude Limit	18,000 meters (60,000 feet) max
Velocity Limit	515 meters /second (1,000 knots) max
Acceleration Limit	Less than 4g
Jerk Limit	20m/sec x 3

Protocol

GPS Protocol	Default: NMEA 0183 (Secondary: SiRF binary)
GPS Output format	GGA (1 sec), GSA (1 sec), GSV (5 sec), RMC (1 sec), GLL, VTG is optional

Power

Voltage	3.3V
Power Consumption	90mA

Temperature

Operating	-10°~ 70°C
Humidity	5 to 95% non-condensing

Physical Characteristics

Dimension	93mm x 31mm x 16mm
Memory Size	512MB

× The specification is subject to modification